EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3030	((703/18,15) or (716/4,17)).CCLS.	USPAT	OR	OFF	2007/10/04 16:11
L2	7	(megacell).clm.	US-PGPUB	OR	OFF	2007/10/04 16:12

OCT 0 3 2007

Web Personalized Results **1 - 10** of about **18,600** for **megacells**. (**0.13** seconds)

/gnuradio/trunk/usrp/fpga/**megacells** - GNU Radio - Trac
accum32.bsf, 3.5 kB, 2297, 4 years, yootis: new and updated **megacells**. accum32.cmp, 1.6 kB, 2297, 4 years, yootis: new and updated **megacells** ...
gnuradio.org/trac/browser/gnuradio/trunk/usrp/fpga/**megacells** - 41k -
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Revision 6420 (checked in by zhuochen, 2 weeks ago). New working **megacells**. Property svn:executable set to *. Line. 1, --Copyright (C) 1991-2006 Altera ...
www.gnuradio.org/.../branches/developers/zhuochen/inband/usrp/fpga/**megacells**/fifo_1kx16.inc?rev=6420 - 10k -
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Process and apparatus for placement of **megacells** in ICs design ...
Process and apparatus for placement of **megacells** in ICs design - US Patent 7103865 from Patent Storm. An IC layout containing **megacells** placed in violation ...
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(b) checking whether the ideal clock buffer location overlaps a **megacell**; and ... (c) finding a set of **megacells** that are accessible by a common adjacent ...
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(c) finding a set of **megacells** that are accessible by a common adjacent side .... 1 is a diagram of a group of **megacells** including a balanced clock tree of ...
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EETimes.com - Panel debates merits of embedded FPGA **megacells**
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relationship are examined for the UDAm **megacell** design. technique embracing ECL and BiCMOS on ... a key driver behind the sea-of-**megacells** movement. Other ...
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